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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

<b>In Re Application of:</b>	John C. Pederson
<b>Application No.:</b>	09/590,976
<b>Filed:</b>	June 9, 2000
<b>For:</b>	LED WARNING SIGNAL LIGHT AND LIGHT SUPPORT
<b>Examiner:</b>	Julie Lieu
<b>Group Art Unit:</b>	2632

Box Official Draftsman  
Commissioner for Patents  
Washington, D.C. 20231

Docket No.: N47.2-9184

**TRANSMITTAL OF FORMAL DRAWINGS**

In response to Notice of Allowance mailed on June 13, 2002, attached please find the formal drawings for this application with each sheet indicating the application number and Group Art Unit on the reverse side of the drawings - Number of Sheets: 8;

Respectfully submitted,

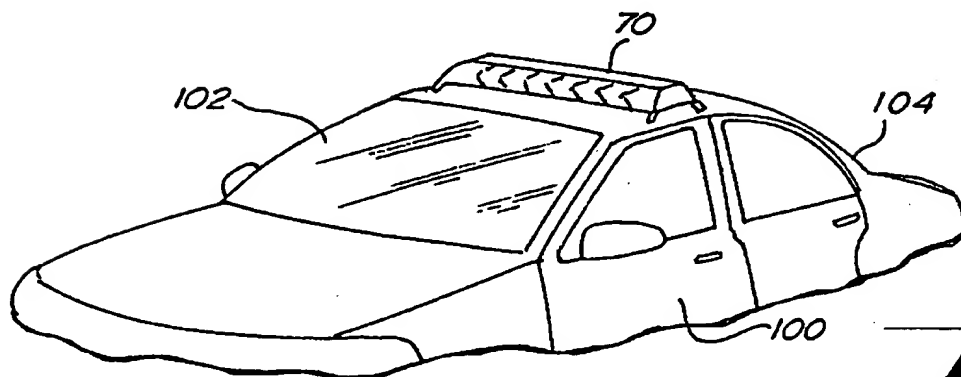
Vidas, Arrett & Steinkraus, P.A.

Date: September 3, 2002

By: \_\_\_\_\_

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*Fig. 1.*

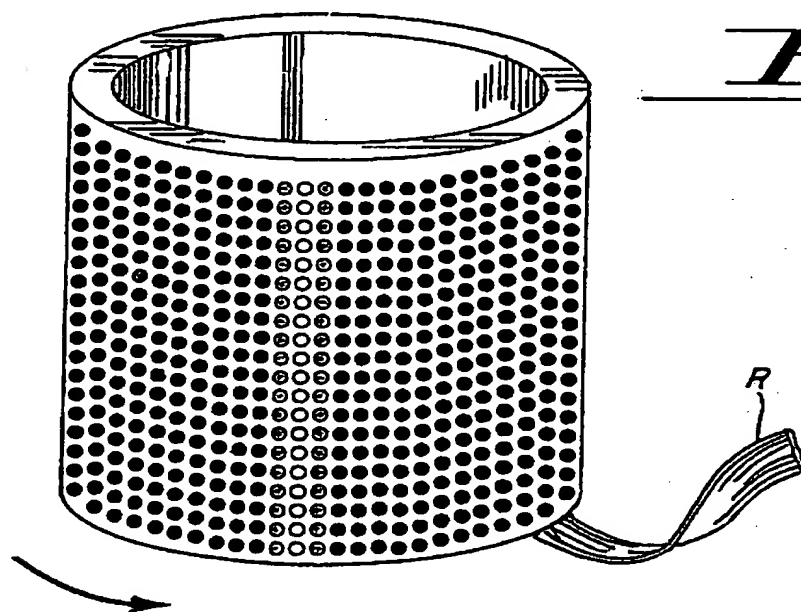
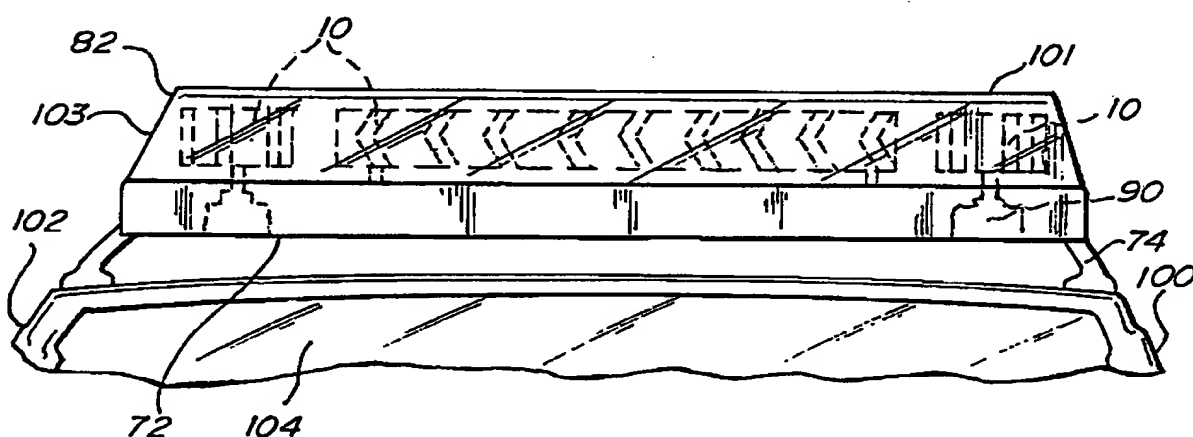
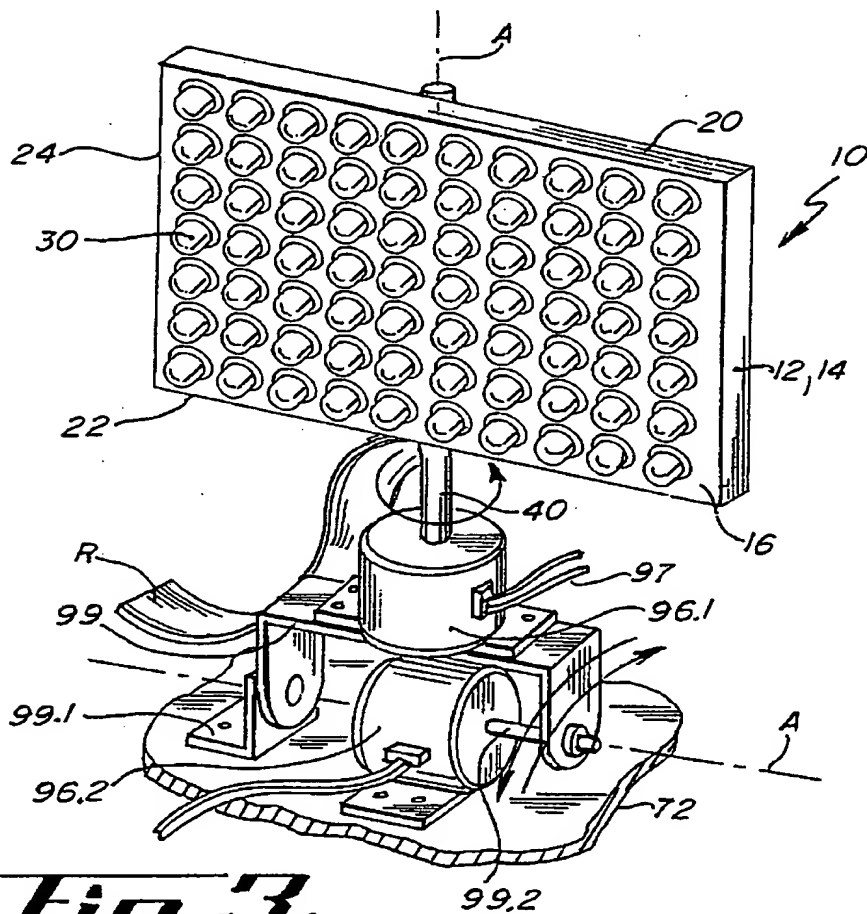


Fig. 4.

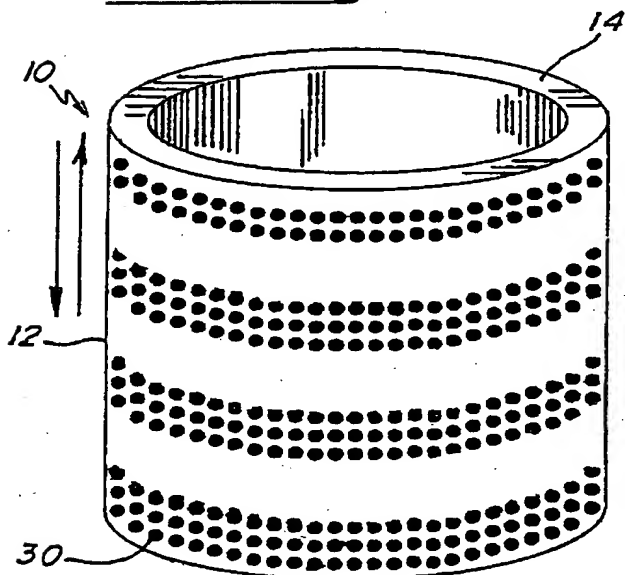


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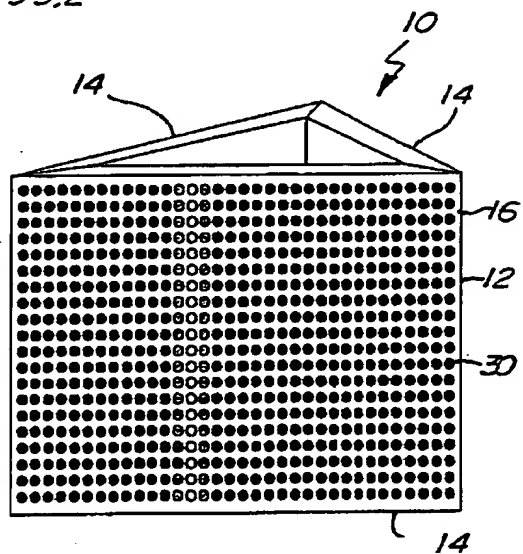
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***Fig. 3.***



***Fig. 5.***

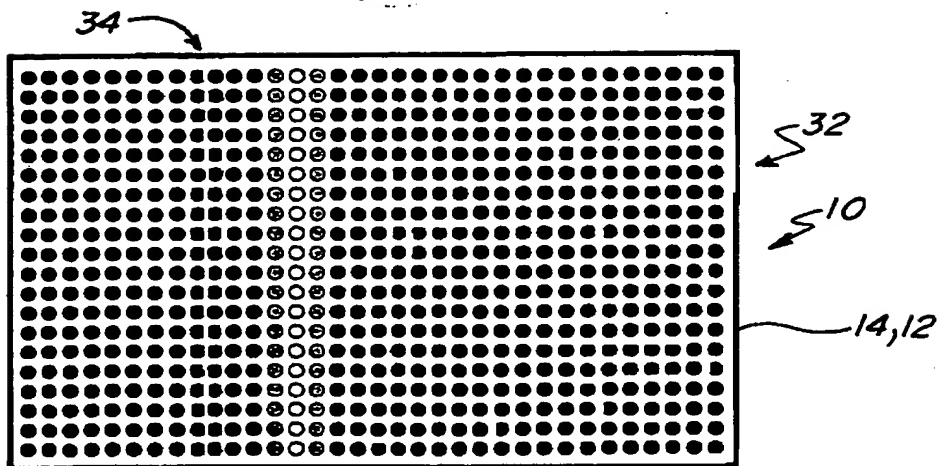


***Fig. 6.***

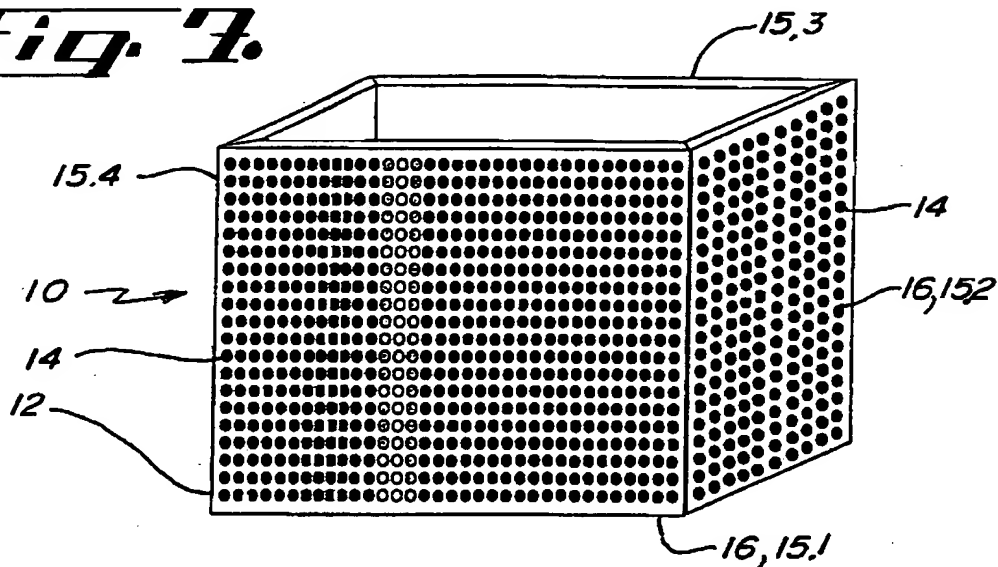


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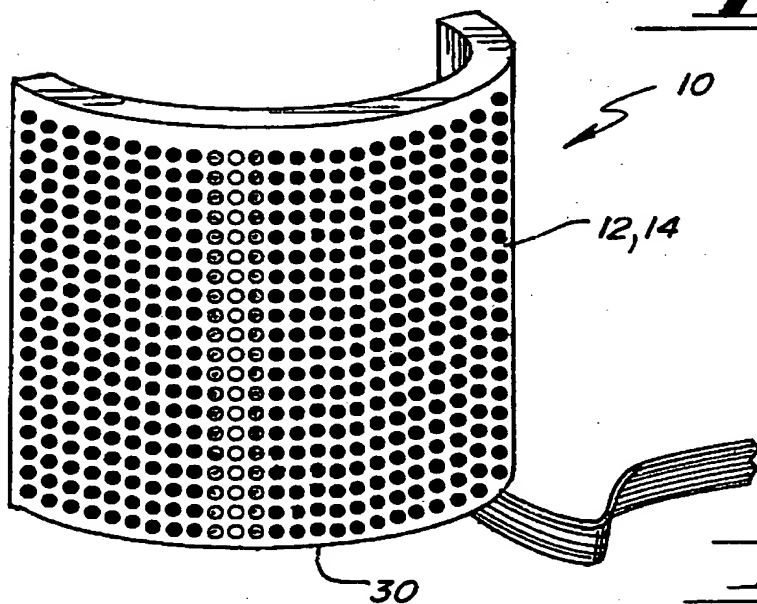
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**Fig. 7.**



**Fig. 8.**



**Fig. 12.**



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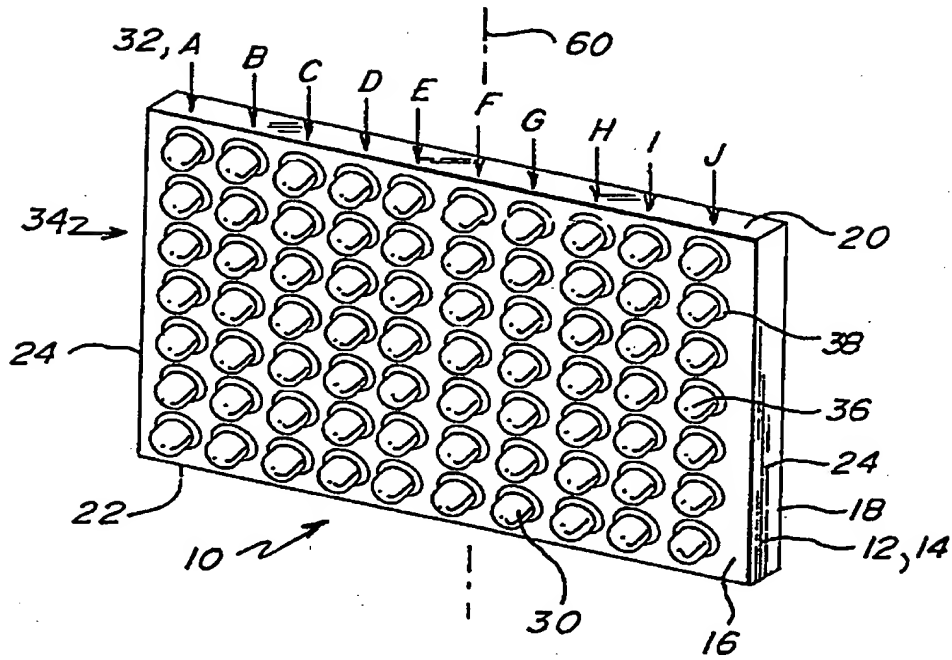


Fig. 9.

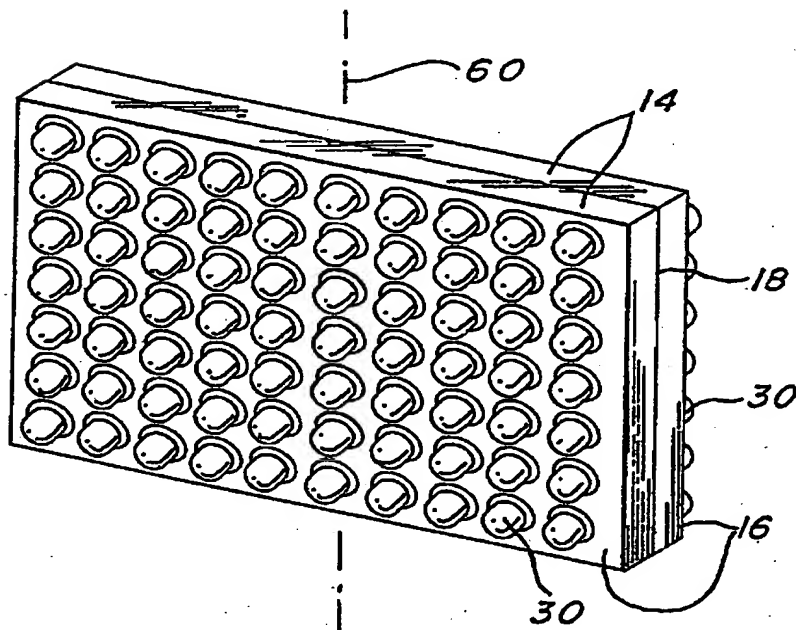
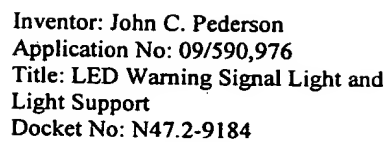


Fig. 10.

The schematic diagram (50) illustrates a microcontroller-based system for monitoring and controlling a power supply (53). The system includes a microcontroller (U-1) with 18 pins, a crystal (XTAL), and various passive components. The microcontroller is connected to a 5V supply and ground. The crystal is connected to pins 14 and 15. The system is connected to a power supply (53) via a diode (D1) and a capacitor (C1). The power supply (53) has two outputs: II and JJ. The system is connected to a power supply (53) via a diode (D1) and a capacitor (C1). The power supply (53) has two outputs: II and JJ.

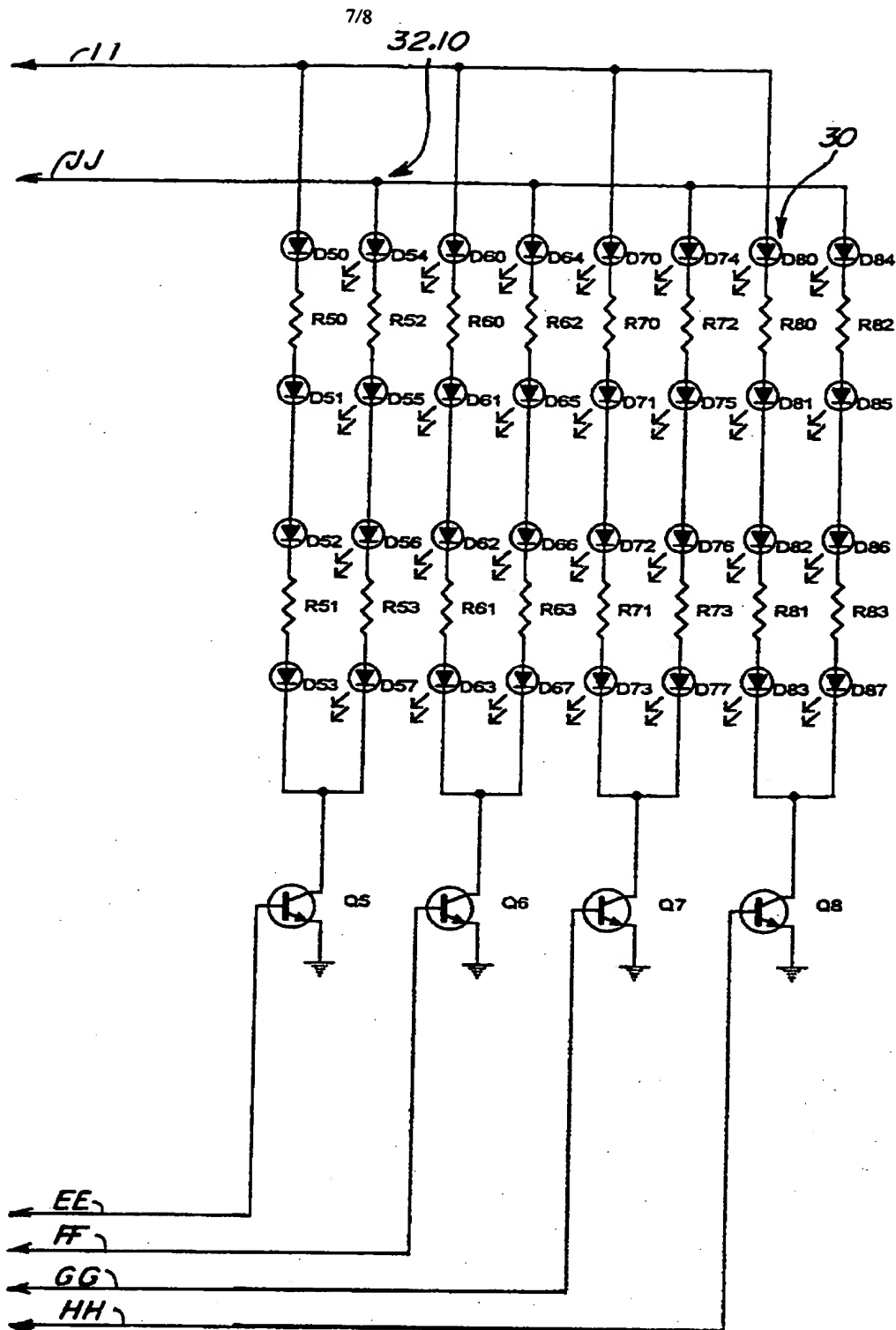


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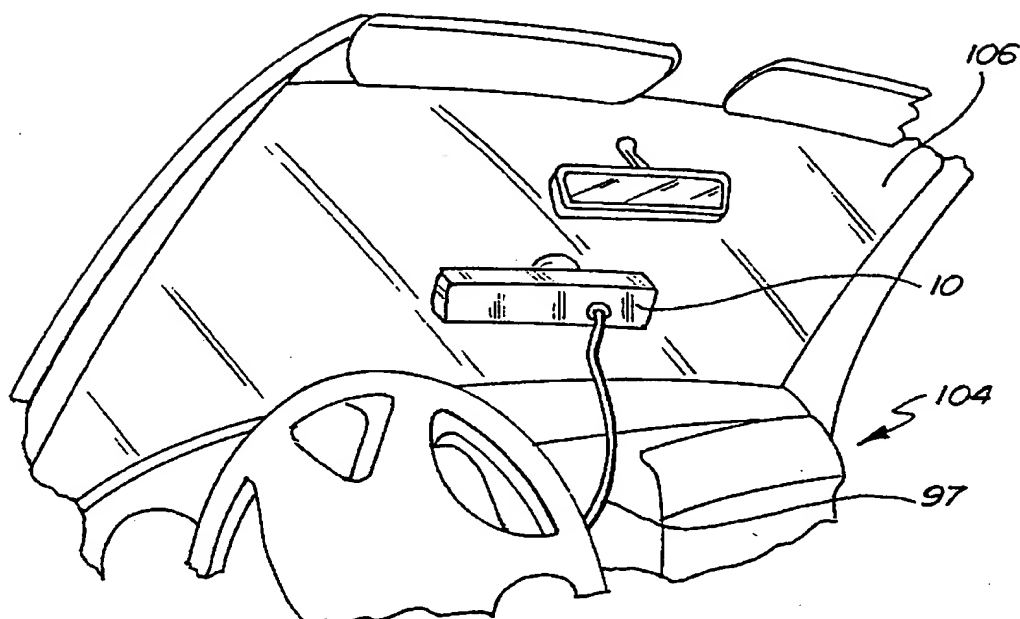
**Fig. 11C.**



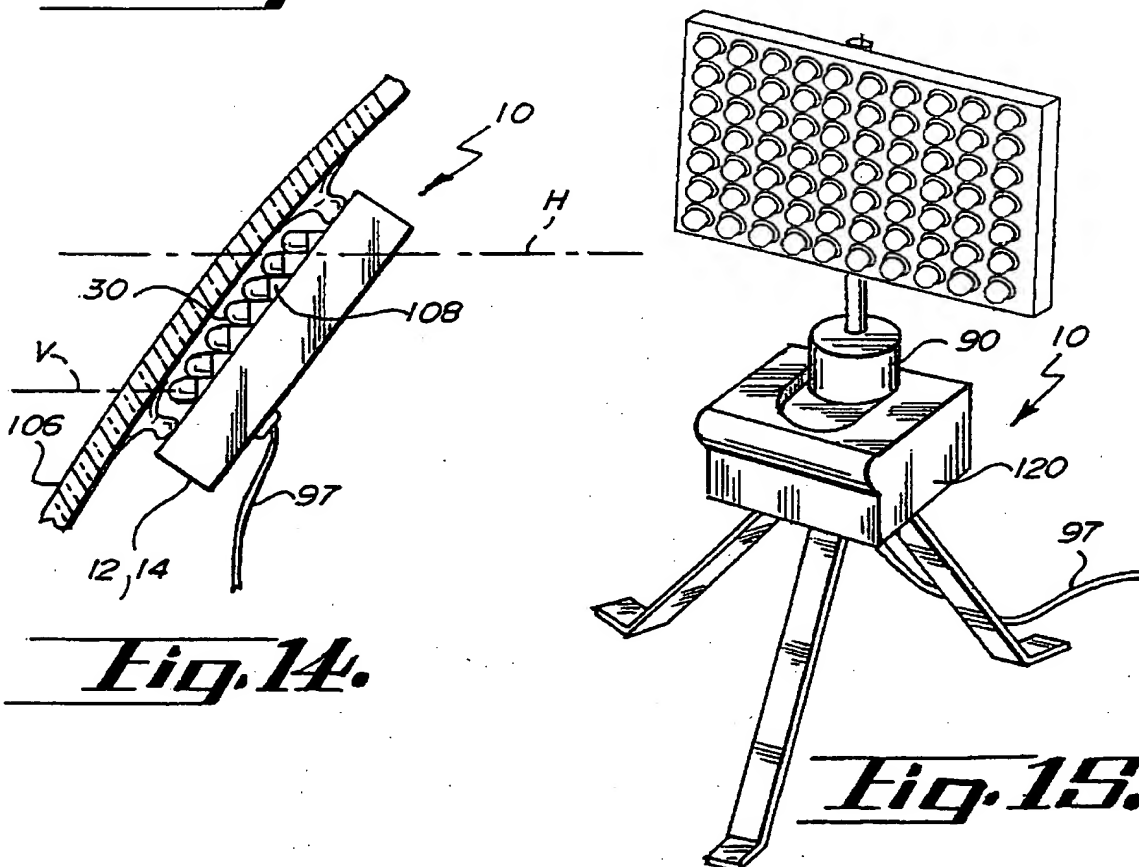


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**Fig. 13.**



**Fig. 14.**

**Fig. 15.**